

Attorney's Docket No. 042390.P9774

#10 2127
PATENT
03-11-04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Erik Cota-Robles, et al.

Application No.: 09/823,482

Filed: March 30, 2001

For: METHOD AND APPARATUS FOR
CONSTRUCTING HOST PROCESSOR SOFT
DEVICES INDEPENDENT OF THE HOST
PROCESSOR OPERATING SYSTEM

Examiner: Lilian Vo

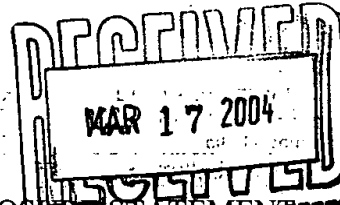
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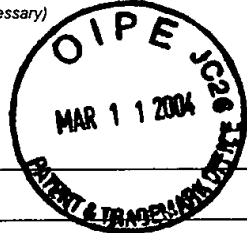
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STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	09/823,482
Filing Date	March 30, 2001
First Named Inventor	Cota-Robles, Erik
Group Art Unit	2127
Examiner Name	Vo, Lilian

Sheet 1 of 1

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US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate

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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date
	EP-EP0473913	03/11/1992	Farrell, Joel A.			

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No *	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Other Document No
		BRANDS, STEFAN, "Restrictive Blinding of Secret-Key Certificates", SPRINGER-VERLAG XP002201306, (1995), Chapter 3	
		DAVIDA, GEORGE I., et al., "Defending Systems Against Viruses through Cryptographic Authentication", Proceedings of the Symposium on Security and Privacy, IEEE Comp. Soc. Press, ISBN 0-8186-1939-2, (May 1989),	
		INTEL, "IA-32 Intel Architecture Software Developer's Manual", Volume 3: System Programming Guide, Intel Corporation - 2003, 13-1 through 13-24	
		LUKE, JAHN, et al., "Replacement Strategy for Aging Avionics Computers", IEEE AES Systems Magazine, XP002190614, (March 1999),	
		MENEZES, OORSCHOT, "Handbook of Applied Cryptography", CRC Press LLC, USA XP002201307, (1997), 475	
		RICHT, STEFAN, et al., "In-Circuit-Emulator Wird Echtzeitauglich", Elektronik, Franzis Verlag GMBH, Munchen, DE, Vol. 40, No. 16, XP000259620, (100-103), 8-6-1991	
		SAEZ, SERGIO, et al., "A Hardware Scheduler for Complex Real-Time Systems", Proceedings of the IEEE International Symposium on Industrial Electronics, XP002190615, (July 1999), 43-48	
		SHERWOOD, TIMOTHY, et al., "Patchable Instruction ROM Architecture", Department of Computer Science and Engineering, University of California, San Diego, La Jolla, CA, (Nov. 2001),	

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Substitute Disclosure Statement Form (PTO-1449)

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Respectfully submitted,

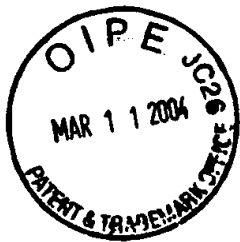
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 8-27, 2004

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Serial/Patent No.: 09/823,482 Filing/Issue Date: March 30, 2001
Client: Intel Corporation
Title: METHOD AND APPARATUS FOR CONSTRUCTING HOST PROCESSOR SOFTWARE DEVICES INDEPENDENT OF THE HOST PROCESSOR OPERATING SYSTEM
BSTZ File No.: 042390.P9774 Atty/Secty Initials: MGP/mb
Date Mailed: 3/19/04 Docket Due Date: ***

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